

1201.63407



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

PATENT

Applicants: Nayfeh et al.)
Serial No.: 09/496,506)
Filed: February 2, 2000)
For: SILICON NANOPARTICLE)
FIELD EFFECT TRANSISTOR AND)
TRANSISTOR MEMORY DEVICE)
Art Unit: 1741)

I hereby certify that this paper is being deposited with the United States Postal Service as FIRST-CLASS mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231, on this date.

May 3, 2001

Date

Arik B. R.

Registration No. 43,874

F-CLASS.WCM

Appr. February 20, 1998

Attorney for Applicant

Assistant Commissioner for Patents
Washington, DC 20231

Sir:

(X) Enclosed is a Supplemental IDS, PTO Form 1449 and cited references to be filed in the above-referenced application.

(X) If a Petition under 37 C.F.R. §1.136(a) for an extension of time for response is required to make the attached response timely and does not separately accompany this transmittal, Applicant(s) hereby petition(s) under 37 C.F.R. §1.136(a) for an extension of time for response in the above-identified application for the period required to make the attached response timely.

(X) The Commissioner is hereby authorized to charge any additional fees which may be required to this application under 37 C.F.R. §§1.16-1.17, or credit any overpayment, to Deposit Account No. 07-2069. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

GREER, BURNS & CRAIN, LTD.

Date: May 3, 2001

By: Arik B. R.
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